

## Model Question Paper-I (18EC42) with effect from 2021

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### Fourth Semester B.E Degree Examination ANALOG CIRCUITS

TIME: 03 Hours

Max. Marks: 100

Note: Answer any FIVE full questions, choosing at least ONE question from each module.

Module -1			Marks
Q.01	a	Explain the voltage divider biasing for BJTs using single power supply. How does $R_E$ provides a negative feedback action to stabilize the bias current with necessary supporting equations.	08
	b	For CE amplifier circuit $V_{BE}$ is adjusted to yield dc collector current of 1mA. Let $V_{CC}=15V$ , $R_C=10K\Omega$ and $B=150$ . Find voltage gain. If $V_{be}=0.002 \sin \omega t$ volt Find $V_c(t)$ and $I_b(t)$ .	08
	c	A BJT having $B=100$ is biased at dc collector current of 0.5mA. Find the value of $g_m$ and $r_{\pi}$ at base point.	04
OR			
Q.02	a	Draw the small signal equivalent circuit model for MOSFET and obtain the expression for voltage gain.	06
	b	Design a circuit to fix $V_G$ and bias using $R_s$ and voltage divider arrangement to establish drain current of 0.5mA. The MOSFET is specified to have $V_t=1V$ $k(W/L)=1mA/V^2$ .	08
	c	Mention relation between $r_{\pi}$ and $r_e$ .	06
Module-2			
Q. 03	a	With neat circuit diagram and ac equivalent circuit derive expressions for $R_{in}$ , $A_v$ and $R_o$ for a CS amplifier with bypassed source resistance.	07
	b	Obtain the expression for $R_i$ , $A_v$ and $R_o$ for a common drain amplifier using suitable ac equivalent circuit.	07
	c	Explain biasing of MOSFET by fixing $V_{GS}$	06
Module-3			
Q.04	a	Obtain the low frequency response of CS amplifier.	10
	b	Draw the circuit of a RC phase shift oscillator using MOSFET and explain the working.	06
	c	A 2MHz quartz crystal is specified to have $L=0.5H$ , $C_s=0.012pF$ , $C_p=4pF$ , $R=120ohm$ Find $f_s$ and $f_p$ .	04
Module-3			
Q. 05	a	Explain Gain Density and effect on Bandwidth with the application of negative feedback in amplifiers.	07
	b	A feedback amplifier produces an output of 10V with an input of 1V. When the feedback is removed to produce same output, input needed is 0.2V.. Find the voltage gain with and without feedback and feedback factor.	07

	c	Define different types of power amplifiers.	<b>06</b>
OR			
Q. 06	a	With a neat block diagram explain the working of a voltage series feedback amplifier. Obtain the expression for gain, Input resistance and output resistance with feedback.	<b>12</b>
	b	Draw the circuit of a transformer coupled Class-A power amplifier. Prove that the maximum conversion efficiency is 50%.	<b>08</b>
<b>Module-4</b>			
Q. 07	a	Derive the relevant expressions for exact and approximate voltage gain and input resistance for a non inverting amplifier using op-amp.	<b>08</b>
	b	With neat circuit diagram explain op-amp based inverting summing amplifier and averaging amplifier with the relevant expressions for the output.	<b>06</b>
	c	In the circuit of inverting summing amplifier $V_a=1V$ , $V_b=2V$ , $V_c=3V$ $R_a=R_b=R_c=3Kohm$ , $R_f=1Kohm$ and supply voltage =15V. Assuming that op-amp is initially nulled find $V_o$ .	<b>06</b>
OR			
Q. 08	a	Draw the circuit and waveforms for an inverting Schmitt Trigger using op-amp with relevant expressions for $V_{ut}$ , $V_{lt}$ and explain. In this circuit if $R_1=100ohm$ $R_2=3.9kOhm$ $V_{in}=500mV$ P-P sine wave and saturation voltage=14V find $V_{ut}$ , $V_{lt}$ and $V_{hy}$ .	<b>10</b>
	b	The 741C is configured as a non-inverting amplifier and following data are given for the circuit. $A=400,000$ $R_i=33Mohm$ $R_o=60ohm$ $R_1=470ohm$ $R_f=4.7kohm$ Supply=15V, $U_{GB}=0.6MHz$ Compute the closed loop parameters $A_f, R_{if}, R_{of}, F_f$ .	<b>10</b>
<b>Module-5</b>			
Q. 09	a	Explain working of second order LPF. Write the design equations. Design the circuit for cut-off frequency of 1 KHz and draw frequency response of the circuit.	<b>12</b>
	b	Explain the operation of 2-bit DAC using R-2R circuit.	<b>08</b>
OR			
Q. 10	a	What is meant by precision rectification ?Explain with a neat circuit diagram, the working of small signal halfwave precision rectifier using op-amp.	<b>08</b>
	b	With the help of a neat circuit diagram and waveforms explain the working of astable circuit operation using 555 timer IC. Derive the expression for $T_{on}$ , $T_{off}$ and $T$ .	<b>12</b>

Table showing the Bloom's Taxonomy Level, Course Outcome and Program Outcome				
Question		Bloom's Taxonomy Level attached	Course Outcome	Program Outcome
Q.1	(a)	L1	CO 01	
	(b)	L2	CO 01	
	(c)	L3	CO 01	

<b>Q.2</b>	(a)	L1	CO 01	
	(b)	L2	CO 01	
	(c)	L3	CO 01	
<b>Q.3</b>	(a)	L2	CO 02	
	(b)	L2	CO 02	
	(c)	L3	CO 02	
<b>Q.4</b>	(a)	L2	CO 02	
	(b)	L2	CO 02	
	(c)	L3	CO 02	
<b>Q.5</b>	(a)	L2	CO 03	
	(b)	L3	CO 03	
	(c)	L2	CO 03	
<b>Q.6</b>	(a)	L2	CO 03	
	(b)	L3	CO 03	
<b>Q.7</b>	(a)	L2	CO 04	
	(b)	L2	CO 04	
	(c)	L3	CO 04	
<b>Q.8</b>	(a)	L2	CO 04	
	(b)	L3	CO 04	
<b>Q.9</b>	(a)	L2	CO 05	
	(b)	L3	CO 05	
<b>Q.10</b>	(a)	L2	CO 05	
	(b)	L3	CO 05	

<b>Bloom's Taxonomy Levels</b>	<b>Lower order thinking skills</b>		
	Remembering( knowledge):L <sub>1</sub>	Understanding Comprehension): L <sub>2</sub>	Applying (Application): L <sub>3</sub>
	<b>Higher order thinking skills</b>		
	Analyzing (Analysis):L <sub>4</sub>	Valuating (Evaluation): L <sub>5</sub>	Creating (Synthesis): L <sub>6</sub>