Sri Bhagawan Mahaveer Jain Educational & Cultural Trust's

JAIN COLLEGE OF ENGINEERING, BELAGAVI

(Approved by AICTE, New Delhi, Affiliated to VTU, Belagavi, Recognized by GoK)
(Accredited by NBA: Department of Civil, CSE, ECE and EEE)

Estd. 2010

Department of Electrical & Electronics Engineering

TIME TABLE

Class – III SEMESTER **Class Room: C-210** **W.E.F.:07/09/2026**

Day / Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
08.30AM - 09.25AM	1BEE305	1BEE303	1BEE304	1BEE302-B1	1BEE302-B2	1BEE302-B3
09.25AM - 10.20AM	1BEE303	1BEE304	1BEE303			
10.20AM - 10.35AM	SHORT BREAK					
10.35AM - 11.30AM	1BEE301	1BEE302	1BEE301	1BEE304	1BEE304	HOD Meeting /Tutorial
11.30AM - 12.25PM	1BEE302	1BEE305	1BEE302	1BEE302	1BEE301	
12:25PM - 01:00PM	LUNCH BREAK					
01.00PM - 01.55PM	GATE/ Remedial Class	1BEE301	Mentoring	1BEE303	1BEE305	TESLA/IEI Dept. Activities & SEC Activities
01.55PM - 02.50PM	1BEEL306-B1 1BEEL307A-B2	1BEEL306-B2 1BEEL307A-B3	1BEEL306-B3 1BEEL307A-B1	1BEE305	1BCP308	
02.50PM - 03.45PM				1BNSS309		
4.00PM - 5.30PM	1BMATDIP310	1BMATDIP310	–	–	–	

Subject code	Subject Name	Faculty Name
1BEE301	Mathematics-III for EEE	Dr. Ravishankar L
1BEE302	Analog Electronic Circuits	Miss. Priyanka Kothavale
1BEE303	Electric Circuit Analysis	Mr. Nandaraja S H
1BEE304	Digital Logic Circuits	Dr.Venkatratnam C
1BEE305	Transformers and Generators	Mrs. Megha Sankpal
1BEEL306	Transformers and Generators Lab	Mrs. Megha Sankpal
1BEEL307A	Pspice Lab for Circuit Analysis	Mr. Yogesh Chavadi
1BCP308	Community Projects	Mr. Ashok Bhajantri
1BNSS309	NSS	Mr. Yogesh Chavadi
1BMATDIP310	Additional Mathematics-I course for Lateral Entry Students	Mr. Shafiq Ahmed

Class Teacher	Mr. Yogesh Chavadi	Practical batches	
Contact Number	9886790129	Batches	Roll. no
Email	yogesh.chavadi@jainbgm.in	B1	1-20
		B2	21-40
		B3	41 onwards

31/8/26

Coordinator

HOD EEE

31/8/26

2/9/26.

Dean Academics

Principal & Director